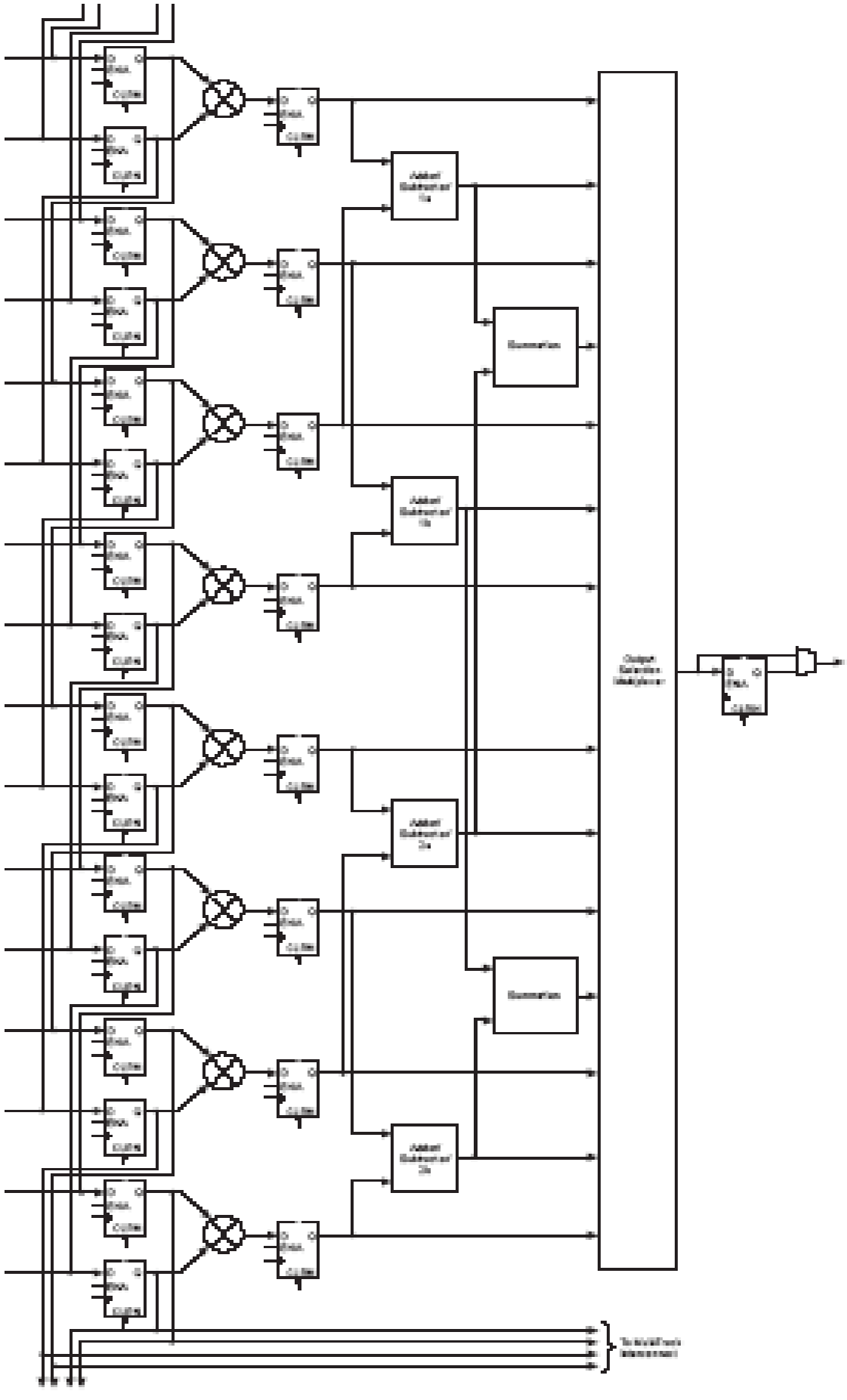Figure 31. DSP Block Diagram for 9 × 9-Bit Configuration