

# Memory Interfaces

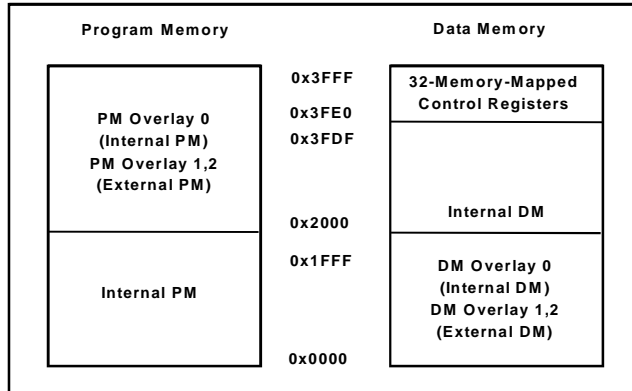


Figure 8-1. ADSP-2181, ADSP-2183, and ADSP-2185 Memory Architecture (MMAP=0 for ADSP-2181 and ADSP-2183, and Mode B=0 for ADSP-2185)

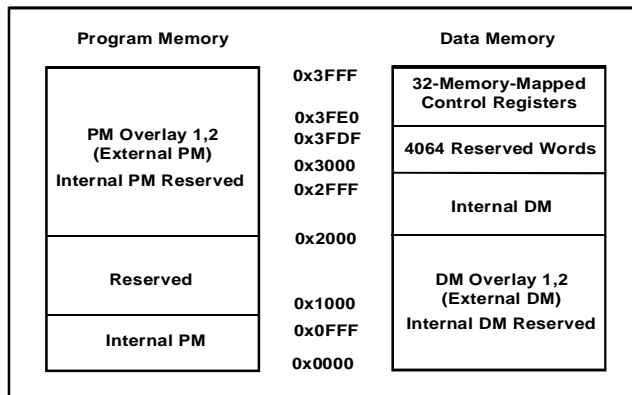


Figure 8-2. ADSP-2184 Memory Architecture (Mode B=0)