

## Non-Memory Mapped Registers

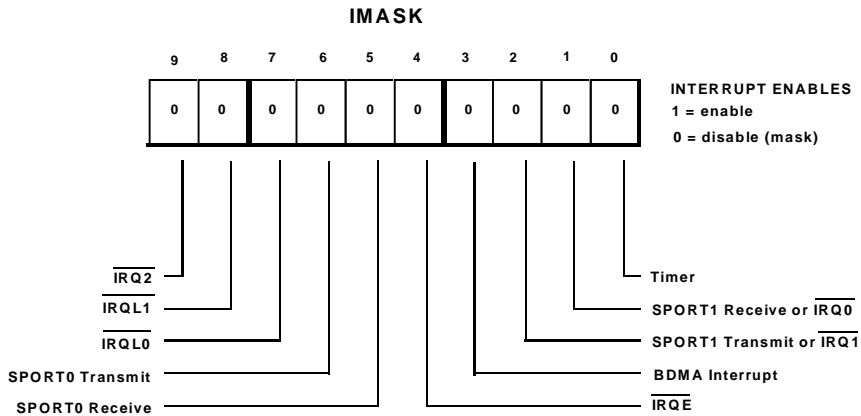


Figure B-25. IMASK Register

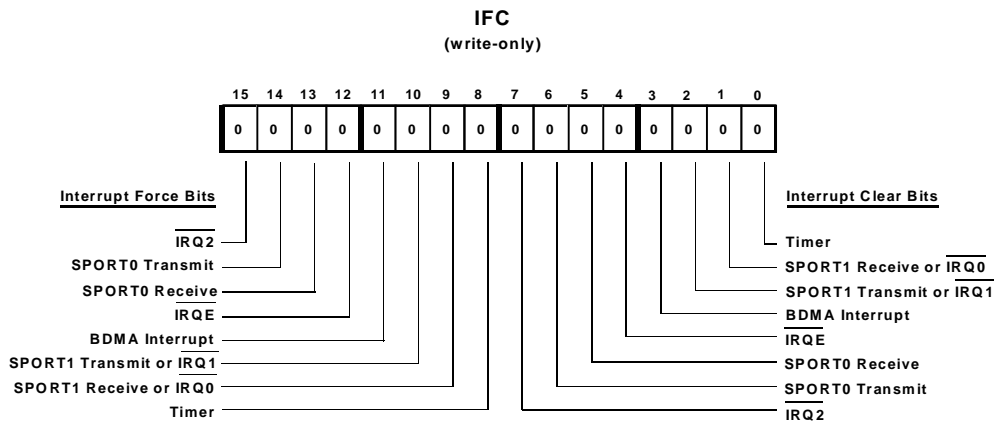


Figure B-26. IFC Register

*Default bit values at reset are shown. If no value is shown, the bit is undefined at reset.  
Reserved bits are shown on a grey field. These reserved bits must be set to zero.*