

# Non-Memory Mapped Registers

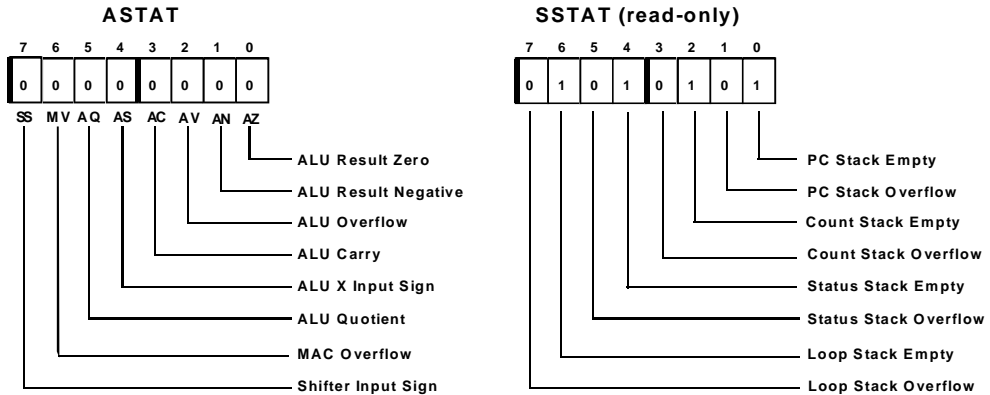


Figure B-23. ASTAT and SSTAT Registers

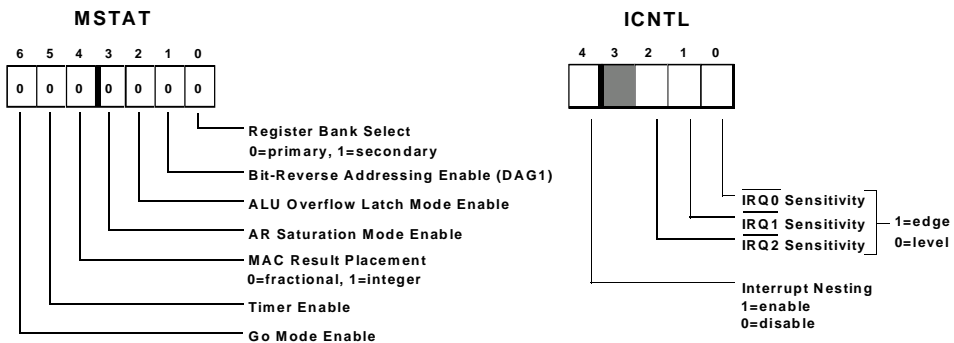


Figure B-24. MSTAT and ICNTL Registers

Default bit values at reset are shown. If no value is shown, the bit is undefined at reset. Reserved bits are shown on a grey field. These reserved bits must be set to zero.